| Component/                                | Fault | Monitor Strategy      | Malfunction                        | Threshold                     | Secondary Parameters | Enable Conditions | Time     | MIL       |
|---|-------|-----------------------|------------------------------------|-------------------------------|----------------------|-------------------|----------|-----------|
| System                                    | Code  | Description           | Criteria                           | Value                         |                      |                   | Required | illum.    |
| Wheel Speed                               |       |                       |                                    |                               |                      |                   |          |           |
| Sensors                                   |       |                       |                                    |                               |                      |                   |          |           |
| Left Front Wheel                          | C1232 | The left front wheel  | WSS feedback voltage <             | 0.20v                         | Sys Voltage          | > 9.0             | > 100ms  | two trips |
| Speed Sensor Circuit<br>Low               |       | speed sensor (WSS)    | Threshold                          | Nominal range:                | Sys Voltage          | < 19.5            |          |           |
| LOW                                       |       | is open.              |                                    | (0.20v < WSS                  | Processing_Enabled   | True (Note 1)     |          |           |
|   |       |                       | Pass Threshold: > 0.20v            | voltage range <<br>2.20v)     | No Active DTCs       | C1207             |          |           |
| Right Front Wheel                         | C1233 | The right front wheel | WSS feedback voltage <             | 0.20v                         | Sys Voltage          | > 9.0             | > 100ms  | two trips |
| Speed Sensor Circuit                      |       | speed sensor is       | Threshold                          | Nominal range:                | Sys Voltage          | < 19.5            |          |           |
| LOW                                       |       | open.                 |                                    | (0.20v < WSS                  | Processing_Enabled   | True (Note 1)     |          |           |
|   |       |                       | Pass Threshold: > 0.20v            | voltage range <               | No Active DTCs       | C1208             |          |           |
|   |       |                       |                                    | 2.20v)                        |                      |                   |          |           |
| Left Rear Wheel                           | C1234 | The left rear wheel   | WSS feedback voltage <             | 0.20v                         | Sys Voltage          | > 9.0             | > 100ms  | two trips |
| Speed Sensor Circuit                      |       | speed sensor is       | Threshold                          | Nominal range:                | Sys Voltage          | < 19.5            |          |           |
| LOW                                       |       | open.                 |                                    | (0.20v < WSS                  | Processing_Enabled   | True (Note 1)     |          |           |
|   |       |                       | Pass Threshold: > 0.20v            | voltage range <               | No Active DTCs       | C1209             |          |           |
|   |       |                       |                                    | 2.20v)                        |                      |                   |          |           |
| Right Rear Wheel                          | C1235 | The right rear wheel  | 5                                  | 0.20v                         | Sys Voltage          | > 9.0             | > 100ms  | two trips |
| peed Sensor Circuit                       |       | speed sensor is       | Threshold                          | Nominal range:                | Sys Voltage          | < 19.5            |          |           |
| LOW                                       |       | open.                 |                                    | (0.20v < WSS                  | Processing_Enabled   | True (Note 1)     |          |           |
|   |       |                       | Pass Threshold: > 0.20v            | voltage range <               | No Active DTCs       | C1210             |          |           |
|   |       |                       |                                    | 2.20v)                        |                      |                   |          |           |
| Left Front Wheel                          | C1207 | The left front wheel  | WSS feedback voltage >             | Threshold1 = 2.20v            | Sys Voltage          | > 9.0             | > 100ms  | one trip  |
| Speed Sensor Circuit<br>High              |       | speed sensor is       | Threshold1                         | Threshold2 = 35ma             | Sys Voltage          | < 19.5            |          |           |
| i ligiti                                  |       | shorted.              | OR                                 | Nominal range:                | Processing_Enabled   | True (Note 1)     |          |           |
|   |       |                       | ORION ASIC detects current >       | (0.20v < WSS                  |                      |                   |          |           |
|   |       |                       | Threshold2                         | voltage range <               |                      |                   |          |           |
|   |       |                       |                                    | 2.20v)                        |                      |                   |          |           |
|   |       |                       | Pass Threshold: < 2.2v             |                               |                      |                   |          |           |
| Right Front Wheel<br>Speed Sensor Circuit | C1208 |                       | WSS feedback voltage >             | Threshold1 = $2.20v$          | Sys Voltage          | > 9.0             | > 100ms  | one trip  |
| High                                      |       | speed sensor is       | Threshold1                         | Threshold2 = 35ma             | Sys Voltage          | < 19.5            |          |           |
|   |       | shorted.              | OR                                 | Nominal range:                | Processing_Enabled   | True (Note 1)     |          |           |
|   |       |                       | ORION ASIC detects current >       | (0.20v < WSS                  |                      |                   |          |           |
|   |       |                       | Threshold2                         | voltage range <               |                      |                   |          |           |
|   |       |                       |                                    | 2.20v)                        |                      |                   |          |           |
| Left Rear Wheel                           | C1209 | The left rear wheel   | Pass Threshold: < 2.2v             | Thrashold 0.000               | Sva Valtaga          |                   | × 100ma  | one trip  |
| Speed Sensor Circuit                      | 01209 | The left rear wheel   | WSS feedback voltage >             | Threshold1 = $2.20v$          | Sys Voltage          | > 9.0             | > 100ms  | one mp    |
| High                                      |       | speed sensor is       | Threshold1<br>OR                   | Threshold2 = 35ma             | Sys Voltage          | < 19.5            |          |           |
|   |       | shorted.              | OR<br>ORION ASIC detects current > | Nominal Range:<br>0.20v < WSS | Processing_Enabled   | True (Note 1)     |          |           |
|   |       |                       |                                    |                               |                      |                   |          |           |
|   |       |                       | Threshold2                         | voltage range < 2.20v         |                      |                   |          |           |
|   |       |                       | Doop Thropholds + 2 2v             | 2.200                         |                      |                   |          |           |
|   |       |                       | Pass Threshold: < 2.2v             | l                             |                      |                   |          |           |

| Component/                                | Fault   |   | Malfunction<br>Critoria  | Threshold   | Secondary Parameters  | Enable Conditions                                   | Time   | MIL                |
|---|---|---|--|---|---|---|--|--------------------|
|   | <b>Code</b><br>C1210  |   | Criteria<br>WSS feedback voltage >   |   | Sys Voltage   | > 9.0   | Required > 100ms   | illum.<br>one trip |
| Speed Sensor Circuit<br>High              |   | speed sensor is shorted.  | Threshold1<br>OR<br>ORION ASIC detects current ><br>Threshold2   | Threshold2 = 35ma<br>Nominal range:<br>(0.20v < WSS<br>voltage range <<br>2.20v)      | Sys Voltage<br>Processing_Enabled   | < 19.5<br>True (Note 1)                             |  |                    |
| Left Front Wheel                          | C1221   | The left front WSS  | Pass Threshold: < 2.2v<br>Number of detected edges = 0   | 0 edges   | Veh Vel   | > 12.8kph   | 20ms   | one trip           |
| signal h<br>out. It h<br>product          | signal has dropped<br>out. It has stopped<br>producing edges. |   | Nominal Range:<br>(N/A)  | System Voltage<br>Processing_Enabled<br>No Active DTCs                                | < 19.5<br>True (Note 1)<br>C1207  | 20110   |  |                    |
|   |   | Missing signal. The<br>left front wheel<br>speed sensor is no<br>longer being<br>detected.  | For Single Missing, TC Active,<br>and Multiple Missing WSS's:<br>Missing Threshold = Larger of:<br>(0.2 x Max)m/s or 1.8m/s<br>Max is the maximum filtered<br>velocity from the other 3<br>wheels<br>Pass Threshold: WSS signal is<br>detected | See Malfunction<br>Criteria<br>Nominal Range:<br>(0.6kph < WSS vel<br>range < 240kph) | Accel (on all wheels)<br>Veh Vel (smallest from all 4 wheels)<br>Processing_Enabled<br>No Active DTCs | < 17.16m/s/s<br>> 12.8kph<br>True (Note 1)<br>C1207 | Single:<br>Time > 5s<br>Single TC<br>Active:<br>Time > 60s<br>Multiple:<br>Time ><br>2minutes<br>/ > 15 ms | one trip           |
| Right Front Wheel<br>Speed Sensor Circuit | C1222   | The right front WSS<br>signal has dropped<br>out. It has stopped<br>producing edges.        | Number of detected edges = 0   | 0 edges<br>Nominal Range:<br>(N/A)  | Veh Vel<br>System Voltage<br>Processing_Enabled<br>No Active DTCs                                     | > 12.8kph<br>< 19.5<br>True (Note 1)<br>C1208       | 20ms   | one trip           |
|   |   | Missing signal. The<br>right front wheel<br>speed sensor is no<br>longer being<br>detected. | For Single Missing, TC Active,<br>and Multiple Missing WSS's:<br>Missing Threshold = Larger of:<br>(0.2 x Max)m/s or 1.8m/s<br>Max is the maximum filtered<br>velocity from the other 3<br>wheels<br>Pass Threshold: WSS signal is<br>detected | See Malfunction<br>Criteria<br>Nominal Range:<br>(0.6kph < WSS vel<br>range < 240kph) | Accel (on all wheels)<br>Veh Vel (smallest from all 4 wheels)<br>Processing_Enabled<br>No Active DTCs | < 17.16m/s/s<br>> 12.8kph<br>True (Note 1)<br>C1208 | Single:<br>Time > 5s<br>Single TC<br>Active:<br>Time > 60s<br>Multiple:<br>Time ><br>2minutes<br>/ > 15 ms | one trip           |
| Left Rear Wheel<br>Speed Sensor Circuit   | C1223   | The left rear WSS<br>signal has dropped<br>out. It has stopped<br>producing edges.          | Number of detected edges = 0   | 0 edges<br>Nominal Range:<br>(N/A)  | Veh Vel<br>System Voltage<br>Processing_Enabled<br>No Active DTCs                                     | > 12.8kph<br>< 19.5<br>True (Note 1)<br>C1209       | 20ms   | one trip           |

| Component/   | Fault | Monitor Strategy  | Malfunction  | Threshold   | Secondary Parameters  | Enable Conditions                                   | Time   | MIL      |
|--|-------|---|--|---|---|---|--|----------|
| System   | Code  | Description   | Criteria   | Value   |   |   | Required   | illum.   |
|  |       | Missing signal. The<br>left rear wheel speed<br>sensor is no longer<br>being detected.                    | For Single Missing, TC Active,<br>and Multiple Missing WSS's:<br>Missing Threshold = Larger of:<br>(0.2 x Max)m/s or 1.8m/s<br>Max is the maximum filtered<br>velocity from the other 3<br>wheels<br>Pass Threshold: WSS signal is<br>detected | See Malfunction<br>Criteria<br>Nominal Range:<br>(0.6kph < WSS vel<br>range < 240kph) | Accel (on all wheels)<br>Veh Vel (smallest from all 4 wheels)<br>Processing_Enabled<br>No Active DTCs | < 17.16m/s/s<br>> 12.8kph<br>True (Note 1)<br>C1209 | Single:<br>Time > 5s<br>Single TC<br>Active:<br>Time > 60s<br>Multiple:<br>Time ><br>2minutes<br>/ > 15 ms | one trip |
| Right Rear Wheel<br>Speed Sensor Circuit                       | C1224 | The right rear WSS<br>signal has dropped<br>out. It has stopped<br>producing edges.                       | Number of detected edges = 0   | 0 edges<br>Nominal Range:<br>(N/A)  | Veh Vel<br>System Voltage<br>Processing_Enabled<br>No Active DTCs                                     | > 12.8kph<br>< 19.5<br>True (Note 1)<br>C1210       | 20ms   | one trip |
|  |       | Missing signal. The<br>right rear wheel<br>speed sensor is no<br>longer being<br>detected.                | For Single Missing, TC Active,<br>and Multiple Missing WSS's:<br>Missing Threshold = Larger of:<br>(0.2 x Max)m/s or 1.8m/s<br>Max is the maximum filtered<br>velocity from the other 3<br>wheels<br>Pass Threshold: WSS signal is<br>detected | See Malfunction<br>Criteria<br>Nominal Range:<br>(0.6kph < WSS vel<br>range < 240kph) | Accel (on all wheels)<br>Veh Vel (smallest from all 4 wheels)<br>Processing_Enabled<br>No Active DTCs | < 17.16m/s/s<br>> 12.8kph<br>True (Note 1)<br>C1210 | Single:<br>Time > 5s<br>Single TC<br>Active:<br>Time > 60s<br>Multiple:<br>Time ><br>2minutes<br>/ > 15ms  | one trip |
| Left Front Wheel<br>Speed Sensor Circuit<br>Range/Performance  | C1225 | Erratic signal. The<br>left front WSS is<br>exhibiting erratic<br>behavior with a large<br>acceleration.  | WSS Accel > Threshold<br>Pass Threshold: < 491m/s/s  | 491m/s/s<br>Nominal Range:<br>(N/A)   | Veh Vel<br>Processing_Enabled<br>No Active DTCs   | > 12.8kph<br>True (Note 1)<br>C1207                 | 280ms<br>Pass >30s   | one trip |
| Right Front Wheel<br>Speed Sensor Circuit<br>Range/Performance | C1226 | Erratic signal. The<br>right front WSS is<br>exhibiting erratic<br>behavior with a large<br>acceleration. | WSS Accel > Threshold<br>Pass Threshold: < 491m/s/s  | 491m/s/s<br>Nominal Range:<br>(N/A)   | Veh Vel<br>Processing_Enabled<br>No Active DTCs   | > 12.8kph<br>True (Note 1)<br>C1208                 | 280ms<br>Pass >30s   | one trip |
| Left Rear Wheel<br>Speed Sensor Circuit<br>Range/Performance   | C1227 | Erratic signal. The<br>left rear WSS is<br>exhibiting erratic<br>behavior with a large<br>acceleration.   | WSS Accel > Threshold<br>Pass Threshold: < 491m/s/s  | 491m/s/s<br>Nominal Range:<br>(N/A)   | Veh Vel<br>Processing_Enabled<br>No Active DTCs   | > 12.8kph<br>True (Note 1)<br>C1209                 | 280ms<br>Pass >30s   | one trip |

|   | Fault<br>Code | Monitor Strategy<br>Description  | Malfunction<br>Criteria  | Threshold<br>Value   | Secondary Parameters  | Enable Conditions   | Time<br>Required    | MIL<br>illum. |
|---|---------------|--|--|--|---|---|---------------------|---------------|
|   | C1228         | Erratic signal. The<br>right rear WSS is<br>exhibiting erratic<br>behavior with a large<br>acceleration. | WSS Accel > Threshold<br>Pass Threshold: < 491m/s/s                    | 491m/s/s<br>Nominal Range:<br>(N/A)  | Veh Vel<br>Processing_Enabled<br>No Active DTCs   | > 12.8kph<br>True (Note 1)<br>C1210   | 280ms<br>Pass >30s  | one trip      |
| Tire Size Mismatch                            | C122E         | This detects that<br>there may be<br>mismatched sized<br>tires on the vehicle                            | WSS (one wheel) –<br>WSS(other 3)  / Wheel<br>Vel(other 3) > Threshold | 25%<br>Nominal Range:<br>N/A   | Vehicle Velocity<br>Cornering<br>Wheel Slip<br>Brake Pedal Apply Detected<br>Processing_Enabled<br>No Active DTCs | >4m/s<br>< 3% (Note 10)<br>Not Detected (Note 10)<br>True (Note 2)<br>True (Note 1)<br>C1207<br>C1208<br>C1209<br>C1210 | 500ms<br>Pass = 60s | one trip      |
| Input Sensors                                 |               |  |  |  |   |   |                     |               |
| Brake Pedal Position<br>Sensor 3 Circuit Low  | C129A         | Brake pedal position<br>3 input signal voltage<br>is low.  | Pass Threshold > 5% of sensor<br>supply voltage                        | 5% of sensor supply<br>voltage<br>(0.25v typically)<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor)  | Sensor Supply Voltage<br>Sensor Supply Voltage<br>Processing_Enabled<br>No Active DTCs                            | > 4.75v<br>< 5.25<br>True (Note 1)<br>C120F   | 75ms                | one trip      |
| Brake Pedal Position<br>Sensor 3 Circuit High | C129B         | Brake pedal position<br>3 input signal voltage<br>is high.   | Pass Threshold > 95% of<br>sensor supply voltage                       | 95% of sensor<br>supply voltage<br>(4.75v typically)<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor) | Sensor Supply Voltage<br>Sensor Supply Voltage<br>Processing_Enabled<br>No Active DTCs                            | > 4.75v<br>< 5.25<br>True (Note 1)<br>C120F   | 75ms                | one trip      |
| Brake Pedal Position<br>Sensor 4 Circuit Low  | C129D         | Brake pedal position<br>4 input signal voltage<br>is low.  | Brake Ped Pos 4 Voltage <<br>Threshold                                 | 5% of sensor supply<br>voltage<br>(0.25v typically)  | Sensor Supply Voltage<br>Sensor Supply Voltage<br>Processing_Enabled<br>No Active DTCs                            | > 4.75v<br>< 5.25<br>True (Note 1)<br>C120F   | 75ms                | one trip      |

| Component/   | Fault | Monitor Strategy  | Malfunction   | Threshold  | Secondary Parameters   | Enable Conditions   | Time  | MIL      |
|--|-------|---|---|--|--|---|---|----------|
| System   | Code  | Description   | Criteria  | Value  |  |   | Required  | illum.   |
| Brake Pedal Position<br>Sensor 4 Circuit High            | C129E | Brake pedal position<br>4 input signal voltage<br>is high.  |   | 95% of sensor<br>supply voltage<br>(4.75v typically)<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>4.5 - 0.5v - Sensor) | Sensor Supply Voltage<br>Sensor Supply Voltage<br>Processing_Enabled<br>No Active DTCs                     | > 4.75v<br>< 5.25<br>True (Note 1)<br>C120F   | 75ms  | one trip |
| Brake Pedal Position<br>Sensor 3 Circuit<br>Offset Error | C129C | The brake pedal<br>position 3 input<br>signal offset voltage<br>is out of range   |   | (>1.07v typical)<br>Nominal Range:   | Brake Pedal Apply Detected<br>OR<br>Pressure Zeroing Enable<br>AND<br>Processing_Enabled<br>No Active DTCs | True (Note 2)<br>True (Note 3)<br>True (Note 1)<br>C120F<br>C127D<br>C129A<br>C129B<br>C12E5<br>C12F8 | 15ms  | one trip |
| Brake Pedal Position<br>Sensor 4 Circuit<br>Offset Error | C129F | The brake pedal<br>position 2 input<br>signal offset voltage<br>is out of range   |   | 5 mm<br>(>1.07v typical)<br>Nominal Range:<br>4.75v - 5.25v -<br>Supply<br>4.5v - 0.5v - Sensor                              | Brake Pedal Apply Detected<br>OR<br>Pressure Zeroing Enable<br>AND<br>Processing_Enabled<br>No Active DTCs | True (Note 2)<br>True (Note 3)<br>True (Note 1)<br>C120F<br>C127D<br>C129D<br>C129E<br>C12E5<br>C120C | 15ms  | one trip |
| Brake Pedal Position<br>Sensor 3 Plausibility            | C12F8 | The brake pedal<br>position 3 input<br>signal does not<br>correlate with the<br>brake pedal position<br>4 signal or with the<br>MC Pressure signal. | correlation table with M/C<br>pressure input<br>Pass Threshold conditions | 0.5v<br>Outside acceptance<br>table (Note 4)<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor)         | Sensor Supply Voltage<br>Sensor Supply Voltage<br>Processing_Enabled<br>No Active DTCs                     | > 4.75v<br>< 5.25<br>True (Note 1)<br>C120F<br>C127D<br>C129A<br>C129B<br>C129C<br>C12E5              | 30ms<br>(condition 1)<br>150ms<br>(condition 2) | one trip |

| Component/<br>System   | Fault<br>Code | Monitor Strategy<br>Description   | Malfunction<br>Criteria  | Threshold<br>Value   | Secondary Parameters   | Enable Conditions   | Time<br>Required                                 | MIL<br>illum. |
|--|---------------|---|--|--|--|---|--|---------------|
| Brake Pedal Position<br>Sensor 4 Plausibility  | C120C         | The brake pedal<br>position 4 input<br>signal does not<br>correlate with the<br>brake pedal position<br>3 signal or with the<br>MC Pressure signal. | Brake Ped Pos 4 input) -<br>Sensor_Supply_Voltage} <<br>Threshold  | 0.5v<br>Outside acceptance<br>table (Note 4)<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor) | Sensor Supply Voltage<br>Sensor Supply Voltage<br>Processing_Enabled<br>No Active DTCs | > 4.75v<br>< 5.25<br>True (Note 1)<br>C120F<br>C127D<br>C129D<br>C129E<br>C129F<br>C129F<br>C12E5   | 30ms<br>(condition 1)<br>150ms<br>(condition 2)  | one trip      |
| ABS Master Cylinder<br>Pressure Sensor<br>Circuit Open or<br>Shorted Low                 | C12B2         | Out of range Low<br>The MCP sensor is<br>either open or<br>shorted to ground.   |  | 5%<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor)   | Processing_Enabled   | True (Note 1)   | 100ms  | one trip      |
| ABS Master Cylinder<br>Pressure Sensor<br>Circuit Shorted High                           | C12B3         | The MCP sensor<br>signal is shorted<br>high.  | MCP Voltage > Supply<br>Threshold<br>Pass Threshold: < 95%   | 95%<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor)  | Processing_Enabled   | True (Note 1)   | 100ms  | one trip      |
| ABS Master Cylinder<br>Pressure Sensor and<br>Brake Pedal Position<br>Sensor Correlation | C12B1         | The Master Cylinder<br>Pressure sensor<br>reading does not<br>correlate with the<br>pedal travel sensor<br>readings.                                | M/C pressure input outside<br>correlation table with Brake<br>Ped Pos x inputs<br>M/C Pressure has not changed<br>by more than Threshold 1 while<br>pedal travel inputs have<br>changed more than Threshold<br>2 | Outside acceptance<br>table (Note 4)<br>Threshold 1 = 3.49<br>kPa  | Processing_Enabled<br>M/C Pressure signal stable<br>No Active DTCs                     | True (Note 1)<br>True (Note 5)<br>C120C<br>C120F<br>C12B2<br>C12B3<br>C12B4<br>C128B<br>C128E<br>C127D<br>C129A<br>C129A<br>C129B<br>C129C<br>C129D<br>C129E<br>C129F<br>C12E5<br>C12F8 | 150ms<br>(condition 1)<br>100ms<br>(condition 2) | one trip      |

| Component/   | Fault | Monitor Strategy  | Malfunction   | Threshold  | Secondary Parameters   | Enable Conditions   | Time                 | MIL      |
|--|-------|---|---|--|--|---|----------------------|----------|
| System   | Code  | Description   | Criteria  | Value  |  |   | Required             | illum.   |
| ABS Master Cylinder<br>Pressure Sensor<br>Performance      | C12B4 | ohmic fault status<br>has changed since<br>the last time the<br>ohmic check was<br>performed. | Transitions from Valid to<br>Open/Shorted State<br>Pass Threshold: Transitions do<br>not occur. | Successive Loops<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor)             | Processing_Enabled<br>No active DTCs:  | True (Note 1)<br>C12B2<br>C12B3   | 100ms<br>Pass =150ms | one trip |
| ABS Master Cylinder<br>Pressure Sensor<br>Offset Error     | C128B | The MCP sensor's<br>input signal offset is<br>out of range.                                   | MCP Offset > Threshold  | 55.9 kPa<br>(0.7v typically)<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor) | (Brake Switch<br>Veh Accel<br>Pump Motor)<br>or<br>Brake Pedal Apply Detected<br>AND<br>Processing_Enabled<br>No active DTCs:                    | False<br>> 0.4m/s2<br>Not Active<br>True (Note 2)<br>True (Note 1)<br>C12B2<br>C12B3<br>C128E           | 20ms                 | one trip |
| ABS Master Cylinder<br>Pressure Sensor Raw<br>Offset Error | C128E | The MCP sensor's raw offset is out of range.  | MCP Raw Offset > Threshold  | 349 kPa<br>(1.64v typical)<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor)   | Brake Control<br>Vehicle Acceleration<br>Vehicle Velocity<br>Accelerator Pedal Position<br>Brake Switch<br>Processing_Enabled<br>No active DTCs: | False (Note 6)<br>> -0.5m/s/s<br>> 2.0m/s<br>< 10%<br>False<br>True (Note 1)<br>C12B2<br>C12B3<br>C128E | 1s                   | one trip |
| Brake Pedal Position<br>Sensor Power Circuit<br>Low        | C120F | The supply to the pedal position sensor is shorted to ground.                                 | Pedal supply voltage <<br>Threshold<br>Pass Threshold > 0.5v                                    | 0.5v   | Processing_Enabled   | True (Note 1)   | 30ms                 | one trip |
| Brake Pedal Position<br>Sensor Reference<br>Circuit        | C12E5 | Determines if the<br>voltage supply to the<br>pedal sensor is out<br>of range.                | Pedal supply voltage <  | Low = 4.75v<br>High = 5.25v<br>Nominal Range:<br>(N/A)   | Processing_Enabled   | True (Note 1)   | 30ms                 | one trip |
| Internal Pressure<br>Sensors                               |       | 1   |   | 1  | 1  | 1   |                      | I        |

| -  | Fault | Monitor Strategy   | Malfunction  | Threshold  | Secondary Parameters                  | Enable Conditions               | Time<br>Description   | MIL       |
|--|-------|--|--|--|---------------------------------------|---------------------------------|-----------------------|-----------|
| System   | Code  | Description  | Criteria   | Value  |                                       |                                 | Required              | illum.    |
| ABS Sensor<br>Reference Output<br>Circuit                                  | C12E4 | Determines if the<br>internal 5v voltage<br>supply is out of<br>range.   | Internal supply voltage <<br>Threshold Low<br>Internal supply voltage ><br>Threshold High<br>Pass Threshold<br>4.75 < Volt <5.25 | Low = 4.75v<br>High = 5.25v<br>Nominal Range:<br>(N/A)                                   | Processing_Enabled                    | True (Note 1)                   | 30ms                  | one trip  |
| ABS HPA Pressure   | C12B6 | Out of range low.  | HPA Voltage < Threshold  | 5%   | Processing_Enabled                    | True (Note 1)                   | 100ms                 | one trip  |
| Sensor Circuit Open<br>or Shorted Low                                      | 01200 | The HPA pressure   | Pass Threshold: > 5%   | Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor)                     | Flocessing_Enabled                    |                                 | Tooms                 |           |
| ABS HPA Pressure<br>Sensor Circuit<br>Shorted High                         | C12B7 | The HPA pressure<br>sensor signal is<br>shorted high.  | HPA Voltage > Supply<br>Threshold<br>Pass Threshold: < 95%   | 95%<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor)              | Processing_Enabled                    | True (Note 1)                   | 100ms                 | one trip  |
| ABS HPA Pressure<br>Sensor Erratic   | C12B8 | An HPA pressure<br>sensor erratic<br>condition exist if the<br>ohmic fault status<br>has changed since<br>the last time the<br>ohmic check was<br>performed          | Transitions from Valid to<br>Open/Shorted State<br>Pass Threshold: Transitions do<br>not occur.                                  | Successive Loops<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor) | Processing_Enabled<br>No active DTCs: | True (Note 1)<br>C12B6<br>C12B7 | 100ms<br>Pass = 150ms | one trip  |
| ABS Regenerative<br>Axle Pressure Sensor<br>Circuit Open or<br>Shorted Low | C12B9 | The regen axle<br>pressure sensor is<br>either open or<br>shorted to ground.   | Regen Axle Voltage <<br>Threshold<br>Pass Threshold: > 5%  | 5%<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor)               | Processing_Enabled                    | True (Note 1)                   | 100ms                 | two trips |
| ABS Regenerative<br>Axle Pressure Sensor<br>Circuit Shorted High           | C12BA | The regen axle<br>pressure sensor<br>signal is shorted<br>high.  | Regen Axle Voltage > Supply<br>Threshold<br>Pass Threshold: < 95%  | 95%<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor)              | Processing_Enabled                    | True (Note 1)                   | 100ms                 | two trips |
| ABS Regenerative<br>Axle Pressure Sensor<br>Erratic                        | C12BB | A regen axle<br>pressure sensor<br>erratic condition<br>exist if the ohmic<br>fault status has<br>changed since the<br>last time the ohmic<br>check was<br>performed | Transitions from Valid to<br>Open/Shorted State<br>Pass Threshold: Transitions do<br>not occur.                                  | Successive Loops<br>Nominal Range:<br>(4.75v - 5.25v -                                   | Processing_Enabled<br>No active DTCs: | True (Note 1)<br>C12B9<br>C12BA | 100ms<br>Pass = 150ms | two trips |

| Component/   | Fault | Monitor Strategy   | Malfunction   | Threshold  | Secondary Parameters   | Enable Conditions   | Time                  | MIL       |
|--|-------|--|---|--|--|---|-----------------------|-----------|
| System   | Code  | Description  | Criteria  | Value  |  |   | Required              | illum.    |
| ABS Regenerative<br>Axle Pressure Sensor<br>Raw Offset Error | C128F | The regen axle<br>pressure sensor's<br>raw offset is out of<br>range.  | > Threshold   | 349 kPa<br>(1.64v typical)<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor)   | Brake Control<br>Vehicle Acceleration<br>Vehicle Velocity<br>Accelerator Pedal Position<br>Brake Switch<br>Processing_Enabled<br>No active DTCs: | False (Note 6)<br>> -0.5m/s/s<br>> 2.0m/s<br>< 10%<br>False<br>True (Note 1)<br>C12B9<br>C12BA<br>C12BB | 1s                    | two trips |
| ABS Regenerative<br>Axle Pressure Sensor<br>Offset Error     | C128C | The regen axle<br>pressure sensor's<br>input signal offset is<br>out of range.   | Regen Axle Signal Offset ><br>Threshold<br>Pass Threshold: < 55.9 kPa                           | 55.9 kPa<br>(0.7v typically)<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor) | Brake Switch<br>Vehicle Acceleration<br>Pump Motor<br>Processing_Enabled<br>No active DTCs:  | False<br>> 0.4m/s2<br>Not Active<br>True (Note 1)<br>C12B9<br>C12BA<br>C12BB                            | 20ms                  | two trips |
| ABS Regenerative<br>Axle Pressure<br>Performance             | C121A | Determines if the<br>regen axle pressure<br>being commanded is<br>being achieved or<br>not.  | Regen Pressure Command) >   | 69.9 kPa<br>Nominal Range:<br>(N/A)  | Regen is in active mode<br>Processing_Enabled<br>No active DTCs:   | True (Note 1)<br>C12B9<br>C12BA<br>C12BB<br>C128F<br>C128C  | 250ms                 | one trip  |
| ABS Boost Pressure<br>Sensor Circuit Open<br>or Shorted Low  | C12BC | The boost pressure<br>sensor is either open<br>or shorted to ground.   | Boost Voltage < Threshold<br>Pass Threshold: > 5%   | 5%<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor)                           | Processing_Enabled   | True (Note 1)   | 100ms                 | one trip  |
| ABS Boost Pressure<br>Sensor Circuit<br>Shorted High         | C12BD | The boost pressure<br>sensor signal is<br>shorted high.  | Boost Voltage > Supply<br>Threshold<br>Pass Threshold: < 95%                                    | 95%<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor)                          | Processing_Enabled   | True (Note 1)   | 100ms                 | one trip  |
| ABS Boost Pressure<br>Sensor Erratic                         | C12BE | A boost pressure<br>sensor erratic<br>condition exist if the<br>ohmic fault status<br>has changed since<br>the last time the<br>ohmic check was<br>performed | Transitions from Valid to<br>Open/Shorted State<br>Pass Threshold: Transitions do<br>not occur. | Successive Loops<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor)             | Processing_Enabled<br>No active DTCs:  | True (Note 1)<br>C12BC<br>C12BD   | 100ms<br>Pass = 150ms | one trip  |

| Component/<br>System                             | Fault<br>Code | Monitor Strategy<br>Description  | Malfunction<br>Criteria   | Threshold<br>Value   | Secondary Parameters   | Enable Conditions   | Time<br>Required | MIL<br>illum. |
|--|---------------|--|---|--|--|---|------------------|---------------|
| ABS Boost Pressure<br>Sensor Raw Offset<br>Error | C128D         | The boost pressure<br>sensor's raw offset<br>is out of range.          | Boost Signal Raw Offset ><br>Threshold  | 349 kPa<br>(1.64v typical)<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor)   | Brake Control<br>Vehicle Acceleration<br>Vehicle Velocity<br>Accelerator Pedal Position<br>Brake Switch<br>Processing_Enabled<br>No active DTCs: | False (Note 6)<br>> -0.5m/s/s<br>> 2.0m/s<br>< 10%<br>False<br>True (Note 1)<br>C12BC<br>C12BD<br>C12BE                   | 1s               | one trip      |
| ABS Boost Pressure<br>Sensor Offset Error        | C128A         | The boost pressure<br>sensor's input signal<br>offset is out of range. |   | 55.9 kPa<br>(0.7v typically)<br>Nominal Range:<br>(4.75v - 5.25v -<br>Supply<br>0.5 - 4.5v - Sensor) | Brake Switch<br>Vehicle Acceleration<br>Pump Motor<br>Processing_Enabled<br>No active DTCs:  | False<br>> 0.4m/s2<br>Not Active<br>True (Note 1)<br>C12BC<br>C12BD<br>C12BE  | 20ms             | one trip      |
| ABS Boost Pressure<br>Performance                | C120A         |  | Boost Pres Diff(BPD) =  Boost<br>Pres(filtered, zeroed) - test<br>command <br>With VSC or TC or ABS<br>active:<br>BPD > Thrshld1<br>Without VSC and TC and ABS<br>active:<br>BPD > Thrshld2 | Thrshld1 = 210 kPa<br>Thrshld2 = 105 kPa<br>Nominal Range:<br>(N/A)                                  | Processing_Enabled<br>No active DTCs:  | True (Note 1)<br>C12B6<br>C12B7<br>C12B8<br>C12BC<br>C12BD<br>C12BE<br>C12BE<br>C128A<br>C128D<br>C127D<br>C127D<br>C12E4 | 500ms            | two trips     |

| Component/  | Fault | Monitor Strategy   | Malfunction  | Threshold  | Secondary Parameters  | Enable Conditions   | Time                 | MIL       |
|---|-------|--|--|--|---|---|----------------------|-----------|
| System  | Code  | Description  | Criteria   | Value  |   |   | Required             | illum.    |
| ABS Boost Pressure<br>Sensor and<br>Regenerative Axle<br>Pressure Sensor<br>Correlation | C12F7 | The regen axle<br>pressure sensor is<br>checked with the<br>boost pressure<br>sensor by equalizing<br>pressure at the two<br>sensors and<br>comparing their<br>difference to a<br>trimmed threshold.<br>The pressures are<br>equalized by<br>controlling the regen<br>axle valves during<br>the test.                        | (Regen axle pressure – Boost<br>pressure)<br>> Threshold<br>Pass Threshold: < 34.9 kPa | 34.9 kPa   | All Wheel Speeds = 0<br>Brake Pedal Apply Detected<br>Boost Pressure<br>Processing_Enabled<br>No active DTCs: | > 1s<br>True (Note 2)<br>> 104.7 kPa<br>True (Note 1)<br>C127D<br>C128A<br>C128C<br>C128D<br>C128F<br>C12B9<br>C12BA<br>C12BB<br>C12BB<br>C12BC<br>C12BD<br>C12BE<br>C12E4<br>C12F7 | 100 ms               | two trips |
| ABS Boost Pressure<br>Loss  | C12FE | This allows the boost<br>control function to<br>keep operating<br>amongst conditions<br>that cause the boost<br>pressure to be<br>limited to less than<br>commanded. The<br>boost control<br>continues to apply<br>until the boost<br>pressure available is<br>no greater than the<br>MC pressure the<br>driver is applying. | Boost Pressure Command<br>AND<br>MC Pressure   | > Boost Pres +<br>105kPa<br>> Boost Pres -<br>14kPa<br>Nominal Range:<br>(N/A) | Boost Pressure<br>PRNDL_State<br>Processing_Enabled<br>No active DTCs:  | < 488.6 kPa<br>!= PARK<br>True (Note 1)<br>C12BC<br>C12BD<br>C12BE<br>C128A<br>C128A<br>C128D<br>C127D<br>C12E4   | 250ms<br>Pass = 30ms | one trip  |
| Hydraulic Control<br>Unit<br>ABS Left Front<br>Isolation Solenoid<br>Driver Shorted     | C12C2 | Whenever the Power<br>Switch Slip Control<br>is closed and the<br>driver transistor is<br>not turned on<br>(solenoid<br>commanded off) the<br>feedback voltage<br>should be high.  | Solenoid feedback voltage <<br>Threshold<br>Pass Threshold: > 30%                      | 30% battery<br>Nominal Range:<br>(8v > 16v)                                    | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command           | True (Note 7)<br>> 8v<br>< 16v<br>Off   | 30ms                 | one trip  |

| Component/  | Fault | Monitor Strategy  | Malfunction   | Threshold                                   | Secondary Parameters  | Enable Conditions                     | Time     | MIL      |
|---|-------|---|---|---|---|---------------------------------------|----------|----------|
| System  | Code  | Description   | Criteria  | Value                                       |   |                                       | Required | illum.   |
| ABS Right Front<br>Isolation Solenoid<br>Driver Shorted | C12C5 | Switch Slip Control<br>is closed and the  | Solenoid feedback voltage <<br>Threshold<br>Pass Threshold: > 30% | 30% battery<br>Nominal Range:<br>(8v > 16v) | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command | True (Note 7)<br>> 8v<br>< 16v<br>Off | 30ms     | one trip |
| ABS Left Front Dump<br>Solenoid Driver<br>Shorted       | C12CC | Whenever the Power<br>Switch Slip Control<br>is closed and the  | Solenoid feedback voltage <<br>Threshold<br>Pass Threshold: > 30% | 30% battery<br>Nominal Range:<br>(8v > 16v) | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command | True (Note 7)<br>> 8v<br>< 16v<br>Off | 30ms     | one trip |
| ABS Right Front<br>Dump Solenoid Driver<br>Shorted      | C12CF |   | Solenoid feedback voltage <<br>Threshold<br>Pass Threshold: > 30% | 30% battery<br>Nominal Range:<br>(8v > 16v) | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command | True (Note 7)<br>> 8v<br>< 16v<br>Off | 30ms     | one trip |
| ABS Right Rear<br>Dump Solenoid Driver<br>Shorted       |       | Whenever the Power<br>Switch Slip Control<br>is closed and the<br>driver transistor is<br>not turned on<br>(solenoid<br>commanded off) the<br>feedback voltage<br>should be high. | Solenoid feedback voltage <<br>Threshold<br>Pass Threshold >30%   | 30% battery<br>Nominal Range:<br>(8v > 16v) | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command | True (Note 7)<br>> 8v<br>< 16v<br>Off | 30ms     | one trip |
| ABS Left Rear Dump<br>Solenoid Driver<br>Shorted        | C12D2 | Whenever the Power<br>Switch Slip Control<br>is closed and the<br>driver transistor is<br>not turned on<br>(solenoid<br>commanded off) the<br>feedback voltage<br>should be high. | Solenoid feedback voltage <<br>Threshold<br>Pass Threshold: > 30% | 30% battery<br>Nominal Range:<br>(8v > 16v) | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command | True (Note 7)<br>> 8v<br>< 16v<br>Off | 30ms     | one trip |

|   | Fault<br>Code | Monitor Strategy<br>Description  | Malfunction<br>Criteria   | Threshold<br>Value  | Secondary Parameters  | Enable Conditions                     | Time<br>Required  | MIL<br>illum. |
|---|---------------|--|---|---|---|---------------------------------------|---|---------------|
|   | C12D8         | Switch Base Brake<br>is closed and the   | Solenoid feedback voltage <<br>Threshold<br>Pass Threshold >30%         | 30% battery<br>Nominal Range:<br>(8v > 16v)   | Power Switch Base Brake Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command   | True (Note 8)<br>> 8v<br>< 16v<br>Off | 30ms  | one trip      |
| ABS Base Brake<br>Closed Solenoid<br>Driver Shorted | C12DB         | Whenever the Power<br>Switch Base Brake<br>is closed and the   | Solenoid feedback voltage <<br>Threshold<br>Pass Threshold >30%         | 30% battery<br>Nominal Range:<br>(8v > 16v)   | Power Switch Base Brake Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command   | True (Note 8)<br>> 8v<br>< 16v<br>Off | 30ms  | one trip      |
| ABS Left Rear Dump<br>Solenoid Circuit<br>Shorted   | C12D1         | Whenever the Power<br>Switch Slip Control<br>is closed and the<br>driver transistor is<br>turned on (solenoid<br>commanded on) the<br>feedback voltage<br>should be low. | Solenoid feedback voltage ><br>Threshold<br>Pass Threshold: < Threshold | 30% of battey<br>(Solenoid in<br>ON/OFF Mode)<br>85% of batter<br>(Solenoid in PWM<br>Mode)<br>Nominal Range:<br>(8v > 16v) | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command | True (Note 7)<br>> 8v<br>< 16v<br>On  | 15ms<br>(Solenoid in<br>ON/OFF<br>Mode)<br>20ms<br>(Solenoid in<br>PWM Mode)<br>Pass = 35ms | one trip      |
| ABS Right Rear<br>Dump Solenoid<br>Circuit Shorted  | C12D4         | Whenever the Power<br>Switch Slip Control<br>is closed and the<br>driver transistor is<br>turned on (solenoid<br>commanded on) the<br>feedback voltage<br>should be low. | Solenoid feedback voltage ><br>Threshold<br>Pass Threshold: < Threshold | 30% of battey<br>(Solenoid in<br>ON/OFF Mode)<br>85% of batter<br>(Solenoid in PWM<br>Mode)<br>Nominal Range:<br>(8v > 16v) | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command | True (Note 7)<br>> 8v<br>< 16v<br>On  | 15ms<br>(Solenoid in<br>ON/OFF<br>Mode)<br>20ms<br>(Solenoid in<br>PWM Mode)<br>Pass = 35ms | one trip      |

|  | Fault<br>Code | Monitor Strategy<br>Description   | Malfunction<br>Criteria  | Threshold<br>Value  | Secondary Parameters  | Enable Conditions                     | Time<br>Required  | MIL<br>illum. |
|--|---------------|---|--|---|---|---------------------------------------|---|---------------|
|  | C12D7         |   | Solenoid feedback voltage ><br>Threshold<br>Pass Threshold: < Threshold  | 30% of battey<br>(Solenoid in<br>ON/OFF Mode)<br>85% of batter<br>(Solenoid in PWM<br>Mode)<br>Nominal Range:<br>(8v > 16v) | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command | True (Note 7)<br>> 8v<br>< 16v<br>On  | 15ms<br>(Solenoid in<br>ON/OFF<br>Mode)<br>20ms<br>(Solenoid in<br>PWM Mode)<br>Pass = 35ms | one trip      |
| ABS Base Brake<br>Closed Solenoid<br>Circuit Shorted | C12DA         | Whenever the Power<br>Switch Base Brake<br>is closed and the<br>driver transistor is<br>turned on (solenoid<br>commanded on) the<br>feedback voltage<br>should be low.            | Solenoid feedback voltage ><br>Threshold<br>Pass Threshold: < Threshold  | 30% of battey<br>(Solenoid in<br>ON/OFF Mode)<br>85% of batter<br>(Solenoid in PWM<br>Mode)<br>Nominal Range:<br>(8v > 16v) | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command | True (Note 7)<br>> 8v<br>< 16v<br>On  | 15ms<br>(Solenoid in<br>ON/OFF<br>Mode)<br>20ms<br>(Solenoid in<br>PWM Mode)<br>Pass = 35ms | one trip      |
| ABS Left Rear Dump<br>Solenoid Circuit Open          | C12D0         | Whenever the Power<br>Switch Slip Control<br>is closed and the<br>driver transistor is<br>not turned on<br>(solenoid<br>commanded off) the<br>feedback voltage<br>should be high. | Solenoid feedback voltage <<br>Threshold<br>Solenoid feedback voltage ><br>Threshold<br>Pass Threshold: > 80%<br>Pass Threshold: < 30% | 80% battery<br>30% battery<br>Nominal Range:<br>(8v > 16v)  | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command | True (Note 7)<br>> 8v<br>< 16v<br>Off | 30ms  | one trip      |
| ABS Right Rear<br>Dump Solenoid<br>Circuit Open      | C12D3         | Whenever the Power<br>Switch Slip Control<br>is closed and the<br>driver transistor is<br>not turned on<br>(solenoid<br>commanded off) the<br>feedback voltage<br>should be high. | Threshold<br>Solenoid feedback voltage ><br>Threshold<br>Pass Threshold: > 80%   | 80% battery<br>30% battery<br>Nominal Range:<br>(8v > 16v)  | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command | True (Note 7)<br>> 8v<br>< 16v<br>Off | 30ms  | one trip      |

| Component/  | Fault | Monitor Strategy  | Malfunction  | Threshold  | Secondary Parameters  | Enable Conditions                     | Time     | MIL      |
|---|-------|---|--|--|---|---------------------------------------|----------|----------|
| System  | Code  | Description   | Criteria   | Value  |   |                                       | Required | illum.   |
| ABS Base Brake<br>Open Solenoid Circuit<br>Open         | C12D6 | Whenever the Power<br>Switch Base Brake<br>is closed and the<br>driver transistor is<br>not turned on<br>(solenoid<br>commanded off) the<br>feedback voltage<br>should be high.     | Solenoid feedback voltage <<br>Threshold<br>Solenoid feedback voltage ><br>Threshold<br>Pass Threshold >80%<br>Pass Threshold <30% | 80% battery<br>30% battery<br>Nominal Range:<br>(8v > 16v)   | Power Switch Base Brake Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command   | True (Note 8)<br>> 8v<br>< 16v<br>Off | 30ms     | one trip |
| ABS Base Brake<br>Closed Solenoid<br>Circuit Open       | C12D9 | Whenever the Power<br>Switch Base Brake<br>is closed and the<br>driver transistor is<br>not turned on<br>(solenoid<br>commanded off) the<br>feedback voltage<br>should be high.     | Threshold<br>Solenoid feedback voltage ><br>Threshold<br>Pass Threshold >80%   | 80% battery<br>30% battery<br>Nominal Range:<br>(8v > 16v)   | Power Switch Base Brake Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command   | True (Note 8)<br>> 8v<br>< 16v<br>Off | 30ms     | one trip |
| ABS Left Rear<br>Isolation Solenoid<br>Circuit Failure  | C12C6 | Whenever the Power<br>Switch Slip Control<br>is closed and the<br>driver transistor is<br>not turned on<br>(solenoid<br>commanded off) the<br>feedback current<br>should be 0 amps. | Current feedback > Threshold<br>Pass Threshold: < 0.10amp  | 0.10amp<br>Nominal Range:<br>(8v > 16v)                      | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command | True (Note 7)<br>> 8v<br>< 16v<br>Off | 30ms     | one trip |
| ABS Left Rear<br>Isolation Solenoid<br>Circuit Shorted  | C12F2 | This failsafe<br>performs the shorted<br>coil detection for HW<br>CLC (Closed Loop<br>Current) coils  |  | 150% of requested<br>current<br>Nominal Range:<br>(8v > 16v) | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command | True (Note 7)<br>> 8v<br>< 16v<br>Off | 15ms     | one trip |
| ABS Right Rear<br>Isolation Solenoid<br>Circuit Failure | C12C8 | Whenever the Power<br>Switch Slip Control<br>is closed and the<br>driver transistor is<br>not turned on<br>(solenoid<br>commanded off) the<br>feedback current<br>should be 0 amps. | Current feedback > Threshold<br>Pass Threshold: < 0.10amp  | 0.10amp<br>Nominal Range:<br>(8v > 16v)                      | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command | True (Note 7)<br>> 8v<br>< 16v<br>Off | 30ms     | one trip |

|   | Fault<br>Code | Monitor Strategy<br>Description   | Malfunction<br>Criteria  | Threshold<br>Value   | Secondary Parameters  | Enable Conditions                                    | Time<br>Required | MIL<br>illum. |
|---|---------------|---|--|--|---|--|------------------|---------------|
| ABS Right Rear<br>Isolation Solenoid<br>Circuit Shorted | C12F5         | This failsafe<br>performs the shorted<br>coil detection for HW<br>CLC coils | Current Feedback > Threshold<br>Pass Threshold: < 150% of<br>requested current | 150% of requested<br>current<br>Nominal Range:<br>(8v > 16v) | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Commanded Current<br>Commanded Current | True (Note 7)<br>> 8v<br>< 16v<br>> 0.25a<br>< 0.35a | 15ms             | one trip      |
| ABS Boost Valve<br>Solenoid Circuit<br>Failure          | C12DC         | Switch Base Brake   | Current feedback > Threshold<br>Pass Threshold < 0.10amp                       | 0.10amp<br>Nominal Range:<br>(8v > 16v)                      | Power Switch Base Brake Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command                             | True (Note 8)<br>> 8v<br>< 16v<br>Off                | 30ms             | one trip      |
| ABS Boost Valve<br>Solenoid Circuit<br>Shorted          | C12DD         | This failsafe is for<br>shorted coil<br>detection for HW<br>CLC coils       | Current Feedback > Threshold<br>Pass Threshold: < 150% of<br>requested current | 150% of requested<br>current<br>Nominal Range:<br>(8v > 16v) | Power Switch Base Brake Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Commanded Current<br>Commanded Current   | True (Note 8)<br>> 8v<br>< 16v<br>> 0.25a<br>< 0.35a | 15ms             | one trip      |
| ABS Proportioning<br>Valve Solenoid Circuit<br>Failure  | C12DE         | Switch Slip Control   | Current feedback > Threshold<br>Pass Threshold <0.10amp                        | 0.10amp<br>Nominal Range:<br>(8v > 16v)                      | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Coil Command                           | True (Note 7)<br>> 8v<br>< 16v<br>Off                | 30ms             | one trip      |
| ABS Proportioning<br>Valve Solenoid Circuit<br>Shorted  | C12DF         | This failsafe is for<br>shorted coil<br>detection for HW<br>CLC coils       | Current Feedback > Threshold<br>Pass Threshold: < 150% of<br>requested current | 150% of requested<br>current<br>Nominal Range:<br>(8v > 16v) | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Commanded Current<br>Commanded Current | True (Note 7)<br>> 8v<br>< 16v<br>> 0.25a<br>< 0.35a | 15ms             | one trip      |

| Component/<br>System                                | Fault<br>Code | Monitor Strategy<br>Description   | Malfunction<br>Criteria   | Threshold<br>Value  | Secondary Parameters  | Enable Conditions                                   | Time<br>Required | MIL<br>illum. |
|---|---------------|---|---|---|---|---|------------------|---------------|
| ABS Left Rear<br>Isolation Solenoid<br>Performance  | C12F3         | The current from the<br>closed loop current<br>controlled valve coil<br>is diagnosed by<br>checking if the<br>difference of the<br>measured current<br>feedback and the<br>commanded current<br>is within a tolerance<br>range. | Coil Feedback Current ><br>Threshold<br>Pass Threshold: < 25% of<br>commanded current | 25% of Commanded<br>Current<br>Nominal Range:<br>(8v > 16v) | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Commanded Current<br>Commanded Current | True (Note 7)<br>> 8v<br>< 16v<br>> 0.0a<br>< 2.5a  | 100ms            | one trip      |
| ABS Right Rear<br>Isolation Solenoid<br>Performance | C12F6         | The current from the<br>closed loop current<br>controlled valve coil<br>is diagnosed by<br>checking if the<br>difference of the<br>measured current<br>feedback and the<br>commanded current<br>is within a tolerance<br>range. | Coil Feedback Current ><br>Threshold<br>Pass Threshold: <25% of<br>Commanded Current  | 25% of Commanded<br>Current<br>Nominal Range:<br>(8v > 16v) | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Commanded Current<br>Commanded Current | True (Note 7)<br>> 8v<br>< 16v<br>> 0.0a<br>< 2.5a  | 100ms            | one trip      |
| ABS Boost Valve<br>Solenoid Circuit<br>Performance  | C12A7         | The current from the<br>closed loop current<br>controlled valve coil<br>is diagnosed by<br>checking if the<br>difference of the<br>measured current<br>feedback and the<br>commanded current<br>is within a tolerance<br>range. | Coil Feedback Current ><br>Threshold<br>Pass Threshold: < 25% of<br>commanded current | 25% of Commanded<br>Current<br>Nominal Range:<br>(8v > 16v) | Power Switch Base Brake Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Commanded Current<br>Commanded Current   | True (Note 8)<br>> 8v<br>< 16v<br>> 0.44a<br>< 1.5a | 100ms            | one trip      |

|  | Fault | 0,  | Malfunction   | Threshold   | Secondary Parameters  | Enable Conditions   | Time     | MIL       |
|--|-------|---|---|---|---|---|----------|-----------|
| System   | Code  | Description   | Criteria  | Value   |   |   | Required | illum.    |
| ABS Proportioning<br>Valve Solenoid<br>Performance | C12F4 | The current from the<br>closed loop current<br>controlled valve coil<br>is diagnosed by<br>checking if the<br>difference of the<br>measured current<br>feedback and the<br>commanded current<br>is within a tolerance<br>range. | Coil Feedback Current ><br>Threshold<br>Pass Threshold <25% of<br>Commanded Current | 25% of Commanded<br>Current<br>Nominal Range:<br>(8v > 16v) | Power Switch Slip Control Enabled<br>Solenoid Power Supply<br>Solenoid Power Supply<br>Commanded Current<br>Commanded Current | True (Note 7)<br>> 8v<br>< 16v<br>> 0.0a<br>< 2.5a                                  | 100ms    | one trip  |
| ABS Pump Motor Run<br>On                           | C12E9 | This fault occurs if<br>the Motor is<br>continuously on for<br>greater than 60s for<br>5 consecutive run<br>times during an<br>ignition cycle.  | FSM Run-On Fault counter ><br>Threshold<br>Pass Threshold < 5                       | 5<br>Nominal Range:<br>(10v > 16v)                          | Motor_Enabled<br>Motor_ON   | True (Note 9)<br>> 60s  | 15 ms    | one trip  |
| ABS Pump Motor<br>Locked                           | C12E8 |   | FS_Motor_No_Edge_Counter<br>< Threshold   | 50<br>Nominal Range:<br>(10v > 16v)                         | Motor_Enabled   | True (Note 9)   | 15 ms    | one trip  |
| Performance  | C12E0 | This fault checks to<br>see if a condition<br>exists in which the<br>accumulator is not<br>charging   | Accumulator Pressure <<br>Threshold<br>Pass Threshold > 839 kPa                     | 769 kPa<br>Nominal Range:<br>(10v > 16v)                    | Brake Pedal Apply Detected<br>Motor_Enabled<br>Boost_Pressure < Command + 105<br>kPa<br>No active DTCs:                       | True (Note 2)<br>True (Note 9)<br>True<br>C12B6<br>C12B7<br>C12B8<br>C127D<br>C12E4 | 100ms    | one trip  |
| Controller   |       |   |   |   |   |   |          |           |
| EBCM Device Voltage<br>Low                         |       | System voltage is too low for certain operations.   | System voltage < Threshold<br>Pass Threshold Volt >9v                               | 9v<br>Nominal Range:<br>(N/A)                               | Ignition  | != Crank  | 100ms    | two trips |
| EBCM Device Voltage<br>High                        | C12E2 | System voltage is too high for certain operations.  | System voltage > Threshold<br>Pass Threshold Volt <16v                              | 16v<br>Nominal Range:<br>(N/A)                              | Ignition  | != Crank  | 100ms    | two trips |

|                      | Fault         |  | Malfunction                   | Threshold            | Secondary Parameters              | Enable Conditions | Time     | MIL       |
|----------------------|---------------|--|-------------------------------|----------------------|-----------------------------------|-------------------|----------|-----------|
|                      | Code<br>C12E6 | Description                            | Criteria                      | Value                |                                   |                   | Required | illum.    |
| Circuit Open         | C12E6         | When the power                         | Voltage Level (switched       | 80% bat voltage      | Power Switch Base Brake Enabled   | True (Note 8)     | 50ms     | one trip  |
| Oneun open           |               | switch has been                        | battery) < Threshold          | Nominal Range:       |                                   |                   |          |           |
|                      |               | commanded on the                       | Deep Threehold, 000/ hot with | (N/A)                | Power Switch Command              | On                |          |           |
|                      |               | 3                                      | Pass Threshold > 80% bat volt |                      |                                   |                   |          |           |
|                      |               | monitored for proper                   |                               |                      |                                   |                   |          |           |
| ABS Power Switch     | C12E7         | operation.                             |                               | Thursday 14 0000     |                                   | 0"                | 50       | on o trin |
| Circuit Shorted      | CIZE/         | The Base Brake                         | Power Switch Short Fault:     | Threshold1 = 80%     | Power Switch Command              | Off               | 50ms     | one trip  |
|                      |               | Power switch                           | Power switch feedback >       | bat volt             | N 4                               |                   |          |           |
|                      |               | voltage decay is                       | Threshold1                    |                      | Motor                             | != Running        |          |           |
|                      |               | monitored after the                    |                               | Threshold $2 = 50\%$ |                                   |                   |          |           |
|                      |               |  | Power Switch Short FSM        | bat volt             |                                   |                   |          |           |
|                      |               | turned off. Voltage                    | Capacitor Fault:              | Nominal Range:       |                                   |                   |          |           |
|                      |               | too high indicates a shorted switch.   | Power switch feedback <       | (N/A)                |                                   |                   |          |           |
|                      |               |  | Threshold2                    |                      |                                   |                   |          |           |
|                      |               | Voltage too low<br>indicates a missing | Pass Threshold                |                      |                                   |                   |          |           |
|                      |               | filter capacitor.                      | 80% < fdbk <50%               |                      |                                   |                   |          |           |
| Traction Control     | C120D         | When the power                         | Voltage Level < Threshold     | 80% voltage          | Power Switch Slip Control Enabled | True (Note 7)     | 50ms     | one trip  |
| Power Switch Circuit | 01200         | switch has been                        | Voltage Level < Threshold     | Nominal Range:       | Fower Switch Slip Control Enabled |                   | 50115    | one uip   |
| Open                 |               | commanded on the                       | Pass Threshold                | (N/A)                | Power Switch Command              | On                |          |           |
|                      |               | voltage level is                       | volt > 80% voltage            | (IN/A)               | Fower Switch Command              | OII               |          |           |
|                      |               | monitored for proper                   | voli > 00 % voliage           |                      |                                   |                   |          |           |
|                      |               | operation.                             |                               |                      |                                   |                   |          |           |
| Traction Control     | C120E         | When the power                         | Voltage Level > Threshold     | 80% voltage          | Power Switch Command              | Off               | 50ms     | one trip  |
| Power Switch Circuit | 0.202         | switch has been                        | Voltage Level > Threshold     | Nominal Range:       | r ower owitch command             |                   | 50113    | ono uip   |
| Shorted              |               | commanded off the                      | Pass Threshold                | (N/A)                |                                   |                   |          |           |
|                      |               | voltage level should                   | volt < 80% voltage            |                      |                                   |                   |          |           |
|                      |               | be at or near zero                     | von < 00 /0 vonage            |                      |                                   |                   |          |           |
|                      |               | volts.                                 |                               |                      |                                   |                   |          |           |
| Controller           |               | Volto                                  |                               |                      |                                   |                   |          |           |
| EBCM Self Test       | C127C         | The Built In Self Test                 | Fail Consecutive Times =      | 2                    |                                   | Upon Starting     | 15ms     | one trip  |
| Failed               |               | (BIST) is responsible                  |                               | -<br>Nominal Range:  |                                   | Scheduler in the  |          |           |
|                      |               | for testing the                        |                               | (N/A)                |                                   | Application       |          |           |
|                      |               | internal functionality                 |                               |                      |                                   |                   |          |           |
|                      |               | of the core within the                 |                               |                      |                                   |                   |          |           |
|                      |               | main microprocessor                    |                               |                      |                                   |                   |          |           |
|                      |               |  |                               |                      |                                   |                   |          |           |
|                      |               |  |                               |                      |                                   |                   |          |           |

| Component/<br>System          | Fault<br>Code |   | Malfunction<br>Criteria                                   | Threshold<br>Value                      | Secondary Parameters | Enable Conditions   | Time<br>Required | MIL<br>illum. |
|-------------------------------|---------------|---|---|---|----------------------|---------------------|------------------|---------------|
| EBCM Processor<br>Performance | C127B         | Normal Operation:<br>The micro sends a<br>watchdog enable<br>command(WEC) via<br>the SPI to the Orion<br>ASIC every schedule<br>loop. If the ASIC<br>does not receive this<br>message, the<br>external watchdog<br>circuit inhibits the<br>power switches.<br>Ignition Self-Test:<br>The external<br>watchdog circuit is<br>tested by not | Power Switch Slip Control<br>Voltage Feedback > Threshold | 80% bat volt<br>Nominal Range:<br>(N/A) |                      | Run during Start-up | 30ms             | one trip      |

| Component/             | Fault | Monitor Strategy                  | Malfunction                      | Threshold       | Secondary Parameters | Enable Conditions | Time     | MIL      |
|------------------------|-------|-----------------------------------|----------------------------------|-----------------|----------------------|-------------------|----------|----------|
| System                 | Code  | Description                       | Criteria                         | Value           |                      |                   | Required | illum.   |
| EBCM Random            | C1255 | The following tests               | If any of the tests fail, the    | See Malfunction |                      | Upon Starting     | 15ms     | one trip |
| Access Memory<br>(RAM) |       |                                   | system is forced into a reset by |                 |                      | Scheduler in the  |          |          |
|                        |       | 1. Read/write of the              | writing an invalid watchdog key  | Nominal Range:  |                      | Application       |          |          |
|                        |       | micro's RAM                       | to the system registers. If the  | (N/A)           |                      |                   |          |          |
|                        |       | registers.                        | RAM failure is NOT detected      |                 |                      |                   |          |          |
|                        |       | 2. Address check of               | by the bootloader static RAM     |                 |                      |                   |          |          |
|                        |       | the RAM address                   | check algorithm then a fault     |                 |                      |                   |          |          |
|                        |       | lines.                            | code is set and the exact type   |                 |                      |                   |          |          |
|                        |       | <ol><li>Verify that the</li></ol> | of RAM failure is written to     |                 |                      |                   |          |          |
|                        |       | RAM location used                 | NVRAM.                           |                 |                      |                   |          |          |
|                        |       | to store the                      |                                  |                 |                      |                   |          |          |
|                        |       | persistent address                |                                  |                 |                      |                   |          |          |
|                        |       | line test address                 |                                  |                 |                      |                   |          |          |
|                        |       | (offset) advances to              |                                  |                 |                      |                   |          |          |
|                        |       | the next address line             |                                  |                 |                      |                   |          |          |
|                        |       | address.                          |                                  |                 |                      |                   |          |          |
|                        |       | <ol> <li>Perform data</li> </ol>  |                                  |                 |                      |                   |          |          |
|                        |       | check on a RAM                    |                                  |                 |                      |                   |          |          |
|                        |       | address that                      |                                  |                 |                      |                   |          |          |
|                        |       | includes a                        |                                  |                 |                      |                   |          |          |
|                        |       | dependency check                  |                                  |                 |                      |                   |          |          |
|                        |       | against another RAM               | 1                                |                 |                      |                   |          |          |
|                        |       | location that is                  |                                  |                 |                      |                   |          |          |
|                        |       | address adjacent to               |                                  |                 |                      |                   |          |          |
|                        |       | the RAM location                  |                                  |                 |                      |                   |          |          |
|                        |       | being tested.                     |                                  |                 |                      |                   |          |          |
|                        |       | 5. Verify that the                |                                  |                 |                      |                   |          |          |
|                        |       | RAM location used                 |                                  |                 |                      |                   |          |          |
|                        |       | to store the                      |                                  |                 |                      |                   |          |          |
|                        |       | persistent data test              |                                  |                 |                      |                   |          |          |
|                        |       | address advances to               |                                  |                 |                      |                   |          |          |
|                        |       | the next test                     |                                  |                 |                      |                   |          |          |

| Component/                     | Fault | Monitor Strategy   | Malfunction               | Threshold   | Secondary Parameters | Enable Conditions                                | Time      | MIL      |
|--------------------------------|-------|--|---------------------------|---|----------------------|--|-----------|----------|
| System                         | Code  | Description  | Criteria                  | Value   |                      |  | Required  | illum.   |
| EBCM Read Only<br>Memory (ROM) | C1256 | This check is called<br>from the scheduler<br>each loop. Each<br>ROM section is<br>check-summed by<br>byte. Each byte will<br>be added to the<br>current checksum for<br>a section. If the byte<br>being checked is the<br>last byte of a<br>section, then the<br>section is verified for<br>a correct checksum.   |                           | 0<br>Nominal Range:<br>(N/A)  |                      | Upon Starting<br>Scheduler in the<br>Application | Immediate | one trip |
| EBCM Stack Overrur             | C126E | To detect underflow<br>and overflow of the<br>system stacks, a<br>word of RAM is<br>reserved at the end<br>of each of the<br>system stacks. A<br>word of RAM is also<br>reserved at the<br>upper-most address<br>of the stack section.<br>The contents of<br>these reserved<br>words will be<br>monitored<br>periodically to<br>determine if they<br>have been modified.<br>To detect cases<br>where the<br>application could be<br>pushing a value onto<br>the stack that<br>matches the test<br>value, the test value<br>that is stored at<br>these reserved<br>addresses will be<br>changed each<br>update. | End of Stack != Threshold | Set value changed<br>every software<br>release<br>Nominal Range:<br>(N/A) |                      | Upon Starting<br>Scheduler in the<br>Application | Immediate | one trip |

| Component/  | Fault | Monitor Strategy  | Malfunction  | Threshold  | Secondary Parameters | Enable Conditions                                | Time                | MIL      |
|---|-------|---|--|--|----------------------|--|---------------------|----------|
| System  | Code  | Description   | Criteria   | Value  |                      |  | Required            | illum.   |
| EBCM Processor<br>Overrun   | C121D | Processor did not<br>perform a proper<br>shutdown. NVRAM<br>blocks written at<br>shutdown do not<br>match expected<br>values upon startup.<br>Processing interrupt<br>occurred.                         | The contents of the two<br>NVRAM blocks are compared<br>upon start-up with expected<br>values from shutdown process. |  |                      | Upon Starting<br>Scheduler in the<br>Application | 15ms                | one trip |
| EBCM<br>Unimplemented<br>Interrupt  | C121E | This fault is set if an<br>interrupt occurs that<br>has no explicit<br>interrupt handler<br>defined.  | Interrupt Set = Threshold  | Not Defined<br>Interrupt Handler<br>Nominal Range:<br>(N/A)      |                      | Upon Starting<br>Scheduler in the<br>Application | 15ms                | one trip |
| EBCM Unexpected<br>Exception  | C121F | This fault is set if an<br>exception that is not<br>supported in our<br>system has been<br>generated.   | Exception Not Supported =<br>Condition   | N/A<br>Nominal Range:<br>(N/A)                                   |                      | Upon Starting<br>Scheduler in the<br>Application | 15ms                | one trip |
| EBCM A/D<br>Conversion Timeout  | C127D | If the Analog to<br>digital converter<br>does not complete<br>its conversion in a<br>set amount of time<br>then this fault is set.  | A/D Conversion Counter =<br>Threshold  | 0 (Counts down<br>from 100)<br>Nominal Range:<br>(N/A)           |                      | Upon Starting<br>Scheduler in the<br>Application | 100 clock<br>cycles | one trip |
| EBCM Non-Volatile<br>Random Access<br>Memory (NVRAM) /<br>Non-volatile RAM  | C12FF | Checksum Error<br>Fault   | NVRAM status bit sent out by<br>core software reports a failed<br>NVRAM  | NVRAMDiagstat > 0<br>Fault Counts > 0<br>Nominal Range:<br>(N/A) |                      | Upon Starting<br>Scheduler in the<br>Application | 15ms                | one trip |
| EBCM Non-Volatile<br>Random Access<br>Memory (NVRAM) /<br>Software Learn ID | C12FF | Software ID held in<br>NVRAM does not<br>match ID hard coded<br>in software   | BB NVRAM SW BLOCK ID<br>~=Software ID  | SwVerIDStat > 0<br>Nominal Range:<br>(N/A)                       |                      | Upon Starting<br>Scheduler in the<br>Application | 15ms                | one trip |
| EBCM High End<br>Timer Performance  | C127A | Execution of the<br>High End Timer<br>(HET) program is<br>limited to the actual<br>instructions of the<br>HET program.<br>Execution of default<br>instructions indicates<br>program execution<br>error. | Default Instructions =<br>Threshold  | Executed<br>Nominal Range:<br>(N/A)                              |                      | Upon Starting<br>Scheduler in the<br>Application | 15ms                | one trip |

| Component/                                 | Fault | Monitor Strategy   | Malfunction  | Threshold  | Secondary Parameters | Enable Conditions                                | Time     | MIL      |
|--|-------|--|--|--|----------------------|--|----------|----------|
| System                                     | Code  | Description  | Criteria   | Value  |                      |  | Required | illum.   |
| EBCM High End<br>Timer Program<br>Overflow | C123B | If the HET program<br>does not complete<br>execution time within<br>one HET loop time,<br>the current HET<br>program is aborted<br>and the next<br>program execution is<br>started and a fault<br>code is set. |  | HET Loop Time<br>Nominal Range:<br>(N/A)               |                      | Upon Starting<br>Scheduler in the<br>Application | 15ms     | one trip |
| EBCM High End<br>Timer (HET) RAM<br>Fault  | C123C | <ul> <li>are continuously ran:</li> <li>1. Read/write of the micro's HET RAM registers.</li> <li>2. Address check of the HET RAM address lines.</li> <li>3. Verify that the</li> </ul>                         | to the system registers. If the<br>RAM failure is NOT detected<br>by the bootloader static RAM<br>check algorithm then a fault<br>code is set and the exact type<br>of RAM failure is written to<br>NVRAM. | See Malfunction<br>Criteria<br>Nominal Range:<br>(N/A) |                      | Upon Starting<br>Scheduler in the<br>Application | 15ms     | one trip |

| Component/<br>System                         | Fault<br>Code | Monitor Strategy<br>Description  | Malfunction<br>Criteria                                 | Threshold<br>Value  | Secondary Parameters | Enable Conditions                                | Time<br>Required | MIL<br>illum. |
|--|---------------|--|---|---|----------------------|--|------------------|---------------|
| EBCM High End<br>Timer (HET)<br>Watchdog     | C123A         | If the HET monitor<br>task is not executed<br>within the allowed<br>time frame, a<br>counter is<br>decremented. When<br>the counter<br>decrements to zero,<br>an interrupt is<br>generated and this<br>fault is set. | Counter = Threshold                                     | 0<br>Nominal Range:<br>(N/A)  |                      | Upon Starting<br>Scheduler in the<br>Application | 15ms             | one trip      |
| EBCM High End<br>Timer Periodic<br>Interrupt | C123E         | This failsafe verifies   | Solenoid Feedback Interrupt<br>from the HET = Threshold | Calculated based on<br>Solenoid activity<br>Nominal Range:<br>(N/A) |                      | Upon Starting<br>Scheduler in the<br>Application | 15ms             | one trip      |
| EBCM Solenoid<br>Timeout                     | C123D         |  | Number of Valid HET<br>Interrupts != Number             | 12<br>Nominal Range:<br>(N/A)                                       |                      | Upon Starting<br>Scheduler in the<br>Application | 15ms             | one trip      |
| CAN /<br>Communications                      |               |  |   |   |                      |  |                  |               |

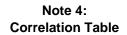
| Component/<br>System                               | Fault<br>Code | Monitor Strategy<br>Description  | Malfunction<br>Criteria   | Threshold<br>Value             | Secondary Parameters | Enable Conditions                                | Time<br>Required | MIL<br>illum. |
|--|---------------|--|---|--------------------------------|----------------------|--|------------------|---------------|
| EBCM Internal<br>Communication Error               | C121C         |  | Secondary micro-processor<br>communication packet does<br>not re-synchonize with<br>expected start-up sequence. | N/A<br>Nominal Range:<br>(N/A) |                      | Upon Starting<br>Scheduler in the<br>Application | 15 ms            | one trip      |
|  |               | If the previous<br>transmission was not<br>completed, then the<br>IPC handler declares<br>an IPC packet<br>transmission overrun<br>failure and disables<br>all IPC<br>communications to<br>introduce the same<br>failure in the other<br>micro. When both<br>nodes are reset then<br>they will re-<br>synchronize. |   |                                |                      |  |                  |               |
|  |               | This fault is set when<br>the attempt to<br>recover from an IPC<br>Transmit Overrun<br>failure was not   |   |                                |                      |  |                  |               |
| EBCM Serial<br>Peripheral Interface<br>Performance | C126F         | 2 data bytes are sent<br>to the Orion ASIC.<br>The Orion sends<br>back the first byte.   | Received Data != Sent Data<br>for Threshold # of attempts   | 3<br>Nominal Range:<br>(N/A)   |                      | Upon Starting<br>Scheduler in the<br>Application | 20 ms            | one trip      |

| Component/           | Fault   | Monitor Strategy       | Malfunction                    | Threshold      | Secondary Parameters | Enable Conditions | Time     | MIL       |
|----------------------|---------|------------------------|--------------------------------|----------------|----------------------|-------------------|----------|-----------|
| System               | Code    | Description            | Criteria                       | Value          |                      |                   | Required | illum.    |
| EBCM Serial          | C123F   | Each time data is      | Counter = Threshold            | 0              |                      | Upon Starting     | 15 ms    | one trip  |
| Peripheral Interface |         | sent out from the      |                                | Nominal Range: |                      | Scheduler in the  |          |           |
| Inoperative          |         | SPI port, a counter is |                                | (N/A)          |                      | Application       |          |           |
|                      |         | loaded. The counter    |                                |                |                      |                   |          |           |
|                      |         | is decremented each    |                                |                |                      |                   |          |           |
|                      |         | check that the micro   |                                |                |                      |                   |          |           |
|                      |         | polls the SPI status   |                                |                |                      |                   |          |           |
|                      |         | to see if the data     |                                |                |                      |                   |          |           |
|                      |         | transfer is complete.  |                                |                |                      |                   |          |           |
|                      |         | The counter should     |                                |                |                      |                   |          |           |
|                      |         | never reach zero       |                                |                |                      |                   |          |           |
|                      |         | before the data        |                                |                |                      |                   |          |           |
|                      |         | transfer is complete.  |                                |                |                      |                   |          |           |
|                      |         | If the counter         |                                |                |                      |                   |          |           |
|                      |         | reaches zero, it       |                                |                |                      |                   |          |           |
|                      |         | means that the         |                                |                |                      |                   |          |           |
|                      |         | peripheral, NVRAM,     |                                |                |                      |                   |          |           |
|                      |         | appears to be non-     |                                |                |                      |                   |          |           |
|                      |         | functional.            |                                |                |                      |                   |          |           |
|                      |         |                        |                                |                |                      |                   |          |           |
|                      |         |                        |                                |                |                      |                   |          |           |
|                      |         |                        |                                |                |                      |                   |          |           |
| EBCM CAN Hardware    | e C12E3 | The hardware           | # of initialization attempts > | 11             |                      | Upon Starting     | 15 ms    | two trips |
| Initialization       |         | confirmation timeout   | threshold                      |                |                      | Scheduler in the  |          |           |
|                      |         | condition is           |                                |                |                      | Application       |          |           |
|                      |         | monitored every time   |                                |                |                      |                   |          |           |
|                      |         | the CAN driver         |                                |                |                      |                   |          |           |
|                      |         | initialization service |                                |                |                      |                   |          |           |
|                      |         | is called. The CAN     |                                |                |                      |                   |          |           |
|                      |         | driver init service is |                                |                |                      |                   |          |           |
|                      |         | called after power     |                                |                |                      |                   |          |           |
|                      |         | up, in Bus Off, or in  |                                |                |                      |                   |          |           |
|                      |         | transmit               |                                |                |                      |                   |          |           |
|                      |         | acknowledgement        |                                |                |                      |                   |          |           |
|                      |         | recovery. The          |                                |                |                      |                   |          |           |
|                      |         | number of counts       |                                |                |                      |                   |          |           |
|                      |         | the CAN driver is      |                                |                |                      |                   |          |           |
|                      |         | allowed to wait for    |                                |                |                      |                   |          |           |
|                      |         | hardware               |                                |                |                      |                   |          |           |
|                      |         | confirmation is 11. If |                                |                |                      |                   |          |           |
|                      |         | the confirmation is    |                                |                |                      |                   |          |           |
|                      |         | not received by this   |                                |                |                      |                   |          |           |
|                      |         | number then the        |                                |                |                      |                   |          |           |
|                      |         | fault is set.          |                                |                |                      |                   |          |           |

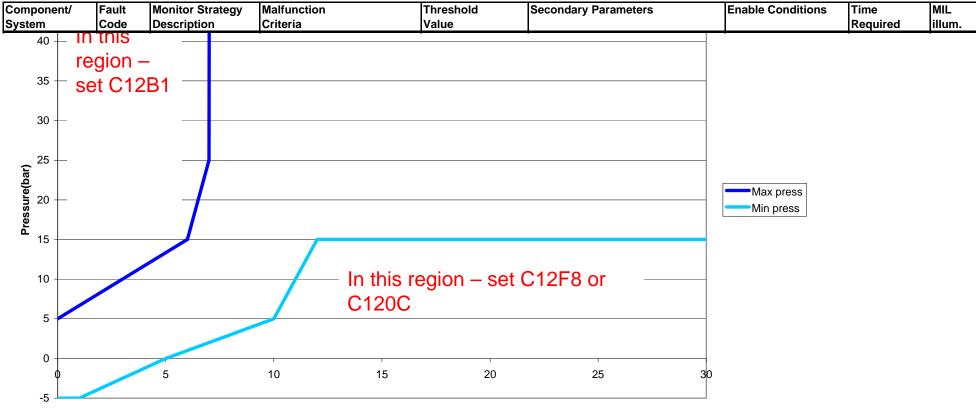
| Component/                                   | Fault | Monitor Strategy   | Malfunction         | Threshold                      | Secondary Parameters | Enable Conditions                                | Time     | MIL       |
|--|-------|--|---------------------|--------------------------------|----------------------|--|----------|-----------|
| System                                       | Code  | Description  | Criteria            | Value                          |                      |  | Required | illum.    |
| Control Module<br>Communication Bus<br>B Off | U180F | The CAN peripheral<br>monitors CAN bus<br>activity and<br>increments an error<br>counter if the<br>following errors are<br>present:<br>1) BIT ERROR: If<br>the bit sent does not<br>match what was<br>expected to be sent,<br>increment the<br>counter.<br>2) STUFF ERROR:<br>This error has to be<br>detected at the bit<br>time of the 6th<br>consecutive equal bit<br>level in a message<br>field that should be<br>coded by the method<br>of bit stuffing.<br>3) CRC ERROR:<br>This error is<br>detected if the<br>calculated result of<br>the receiver is not<br>the same as that<br>received from the<br>transmitter.<br>4) FORM ERROR:<br>This error is | Counter > Threshold | 256<br>Nominal Range:<br>(N/A) |                      | Upon Starting<br>Scheduler in the<br>Application | 15 ms    | two trips |
| EBCM<br>Communication Bus<br>"B" RAM Error   | C126D | The first CAN device<br>does not pass RAM<br>check on the mailbox<br>area. The CAN<br>mailbox RAM check<br>is executed once<br>after power up or<br>reset of the<br>microprocessor.  |                     | 0<br>Nominal Range:<br>(N/A)   |                      | Executed once upon<br>startup                    | 15 ms    | one trip  |

| Component/   | Fault | Monitor Strategy  | Malfunction  | Threshold                               | Secondary Parameters | Enable Conditions                                | Time  | MIL       |
|--|-------|---|--|---|----------------------|--|---|-----------|
| System   | Code  | Description   | Criteria   | Value                                   |                      |  | Required  | illum.    |
| EBCM<br>Communication Bus<br>"B" Performance   | C126C | The CAN frame does<br>not receive<br>acknowledgement<br>for predefined<br>amount of time. If<br>this fault is enabled<br>in the node<br>supervisor then<br>transmit confirmation<br>is expected within<br>200 ms.Transmit<br>request sets the<br>timeout timer and<br>successful<br>transmission resets<br>the timeout timer. | CAN Frame acknowledgement<br>not received  | Not Received<br>Nominal Range:<br>(N/A) |                      | Upon Starting<br>Scheduler in the<br>Application | 200ms   | two trips |
| Antilock Brake<br>System Control<br>Module Lost<br>Communication With<br>Hybrid Powertrain<br>Control Module on<br>Bus B | U1843 | One or more of the<br>Communication<br>messages (3) with<br>the Hybrid<br>Powertrain Control<br>Module are missing.   | The specified input packet with<br>consistent data was not<br>received by COMMS for a<br>predefined time. Every<br>periodic input packet is<br>monitored for input deadline<br>timeout. The deadline timeout<br>is reset each time new packet<br>data is received. The deadline<br>timeout is either set in DBC file<br>or during the configuration of<br>the COMMS subsystem. | Nominal Range:<br>(N/A)                 |                      | Upon Starting<br>Scheduler in the<br>Application | 2.5 times the<br>expected<br>message<br>transmit time | two trips |
| Antilock Brake<br>System Control<br>Module Lost<br>Communication With<br>Engine Control<br>Module on Bus B               | U1842 | Communication<br>message with the<br>Engine Control<br>Module is missing.   | The specified input packet with<br>consistent data was not<br>received by COMMS for a<br>predefined time. Every<br>periodic input packet is<br>monitored for input deadline<br>timeout. The deadline timeout<br>is reset each time new packet<br>data is received. The deadline<br>timeout is either set in DBC file<br>or during the configuration of<br>the COMMS subsystem. |   |                      | Upon Starting<br>Scheduler in the<br>Application | 2.5 times the<br>expected<br>message<br>transmit time | two trips |

| •   | Fault        | Monitor Strategy   | Malfunction  | Threshold                  | Secondary Parameters   | Enable Conditions                                | Time  | MIL       |
|---|--------------|--|--|----------------------------|--|--|---|-----------|
| System  | Code         | Description  | Criteria   | Value                      |  |  | Required  | illum.    |
| Antilock Brake<br>System Control<br>Module Lost<br>Communication With<br>Engine Control<br>Module       | U186A        | Communication<br>message with the<br>Engine Control<br>Module is missing.          | The specified input packet with<br>consistent data was not<br>received by COMMS for a<br>predefined time. Every<br>periodic input packet is<br>monitored for input deadline<br>timeout. The deadline timeout<br>is reset each time new packet<br>data is received. The deadline<br>timeout is either set in DBC file<br>or during the configuration of<br>the COMMS subsystem. | Nominal Range:<br>(N/A)    |  | Upon Starting<br>Scheduler in the<br>Application | 2.5 times the<br>expected<br>message<br>transmit time | two trips |
| Antilock Brake<br>System Control<br>Module Lost<br>Communication With<br>Transmission Control<br>Module | U186B        | Communication<br>message with the<br>Transmission<br>Control Module is<br>missing. | The specified input packet with<br>consistent data was not<br>received by COMMS for a<br>predefined time. Every<br>periodic input packet is<br>monitored for input deadline<br>timeout. The deadline timeout<br>is reset each time new packet<br>data is received. The deadline<br>timeout is either set in DBC file<br>or during the configuration of<br>the COMMS subsystem. | Nominal Range:<br>(N/A)    |  | Upon Starting<br>Scheduler in the<br>Application | 2.5 times the<br>expected<br>message<br>transmit time | two trips |
|   |              |  |  |                            |  |  |   |           |
| Note #1 - Processing_   | Enable is se | t to FALSE when the follow   | ing DTCs are set to 'Fault': C1255, C125   | 6, C126E, C123C, C1270     | 2<br>2   |  |   | 1         |
| Note #2 - Brake Pedal   | Apply Detec  | ted is the determination that  | at the driver has applied the brake pedal.   | It is a combination of ind | ications from the 4 driver inputs: Brake S   | witch, Master Cylinder Pressure, Bra             | ke Pedal Position 3 a                                 | nd Brake  |
| Note #3 - Pressure Ze   | roing Enable | e. When the vehicle is in a  | known state that the driver brake pedal s  | hould be released, the Pr  | essure Zeroing Enable is set. Typical ver  | nicle conditions are:                            |   |           |
| Note #4 - See Correlat  | ion Table be | elow   |  |                            |  |  |   |           |
|   |              |  |  |                            | g (0.5 Hz and 5 Hz.) If all 3 values are wit   |  | e driver's input is con                               | sidered   |
|   |              |  |  |                            | trol valves are being commanded and the  |  |   |           |
|   |              | · · · · · ·  |  |                            | he brake controller. It is set to FALSE whether the set to FALSE whether the set to FALSE whether the set of t |  |   |           |
|   |              |  |  |                            | fety mechanism for the brake controller.   | •  |   |           |
| Note #9 - Motor_Enab  |              |  |  |                            | en the following DTCs are set to 'Fault': C  |  |   |           |
|   |              |  | wheel speeds to estimate the percentage  |                            |  |  |   |           |







Travel(mm at Rod)